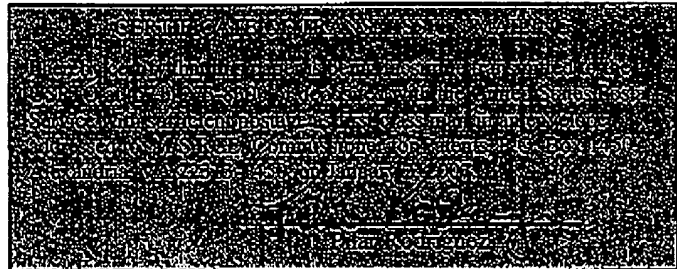


Patent**Attorney Docket No.: Intel 2207/10121**
Assignee: Intel Corporation**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

APPLICANT : Sailesh KOTTAPALLI et al.
SERIAL NO. : 09/751,762
FILED : December 29, 2000
FOR : METHOD FOR CONVERTING PIPELINE
STALLS CAUSED BY INSTRUCTIONS WITH
LONG LATENCY MEMORY ACCESSES TO
PIPELINE FLUSHES IN A MULTITHREADED
PROCESSOR
GROUP ART UNIT : 2183
EXAMINER : Daniel H. PAN

M/S: RCE
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

**AMENDMENT AND REQUEST FOR CONTINUED
EXAMINATION (RCE) UNDER 37 C.F.R. §1.114**

Dear Sir:

The following amendments are associated with and accompanied by a Request for
Continued Examination under 37 C.F.R. §1.114.

Listing of the Claims begin on page 2 of this response.

Remarks/ Arguments begin on page 5 of this response.